

IWNT2026

International Workshop on Nanodevice Technologies 2026 (IWNT2026)
3rd Symposium of Hiroshima University J-Peaks :

New Frontiers through Interdisciplinary Research Integration

国際ナノデバイス技術ワークショップ2026 / 広島大学 J-Peaks 第3回シンポジウム: 学際的研究の融合が切り拓く新たなフロンティア

November 12-13, 2026

Venue: Higashi Hiroshima Arts & Culture Hall Kurara

(Higashi-Hiroshima, Hiroshima, Japan/ JR
Sanyo Main Line Saijo station, 4 min. walk)

東広島芸術文化ホールくらら (JR山陽本線西条駅 徒歩4分)



Poster Application deadline : Oct. 2nd, 2026

Registration deadline : Oct. 23th, 2026

ポスター発表申込締切: 10月2日(金) 参加申込締切: 10月23日(金)

Invited Speakers(Tentative):

Shinichi Takagi,	Teikyo University, Japan	Kazuhiro Nomura,	Micron Memory Japan, Japan
Roger Loo,	IMEC, Belgium	Alessandro Piovaccari,	SigmaSense, USA
Shigeo Asahi,	Kobe University, Japan	Koji Sugano,	Kobe University, Japan
Christopher Bailey,	Arizona State University, USA	Carlos Tokunaga,	Intel, USA
Peter Bermel,	Purdue University, USA	Tomohisa Yoshioka,	Kobe University, Japan
Yoshihiro Hayashi,	AIST, Japan	Carl-Mikael Zetterling,	KTH, Sweden
Masaru Izawa,	Hitachi High-Tech Corp., Japan	Shin-Ichiro Kuroki,	RISE, Hiroshima University, Japan
Alvin Loke,	Intel, USA	Koichi Matsuo,	Hiroshima University, Japan
Noriaki Matsunaga,	Applied Materials Japan, Japan	Kenta Shigaki,	Hiroshima University, Japan

Contact

お申し込み・
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Organizers:

- Research Institute for Semiconductor Engineering (RISE), Hiroshima University
- Setouchi Semiconductor Consortium
- Program for Forming Japan's Peak Research Universities (J-Peaks)
- Networked Exchange, United Strength for Stronger Partnerships between Japan and ASEAN



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